

ABSTRACT OF THE DISCLOSURE

A programmable logic controller connects to an extra memory by a data bus. The memory of the PLC stores an indicating instruction. As the CPU of the PLC reads the indicating instruction, then the CPU reads the address of the extra memory
5 to execute the data codes stored in the extra memory. Thus the programmable logic controller with an extra memory updates or expands the data codes in the extra memory effectively without changing the memory inside the PLC.